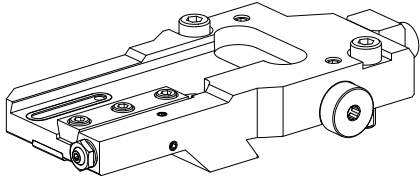


11091545

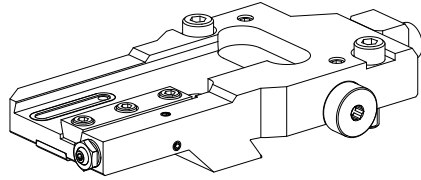
**INDEX**



MS32/MS40

11091545

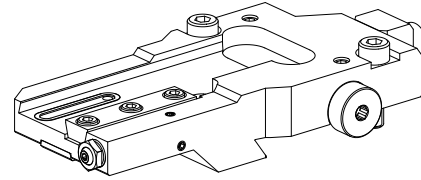
**INDEX**



MS32/MS40

11091545

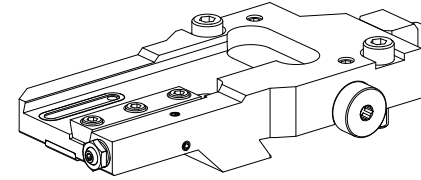
**INDEX**



MS32/MS40

11091545

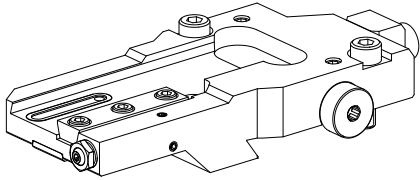
**INDEX**



MS32/MS40

11091545

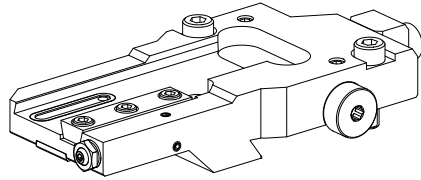
**INDEX**



MS32/MS40

11091545

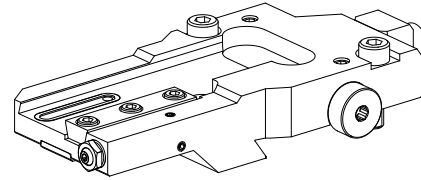
**INDEX**



MS32/MS40

11091545

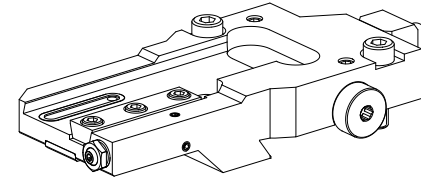
**INDEX**



MS32/MS40

11091545

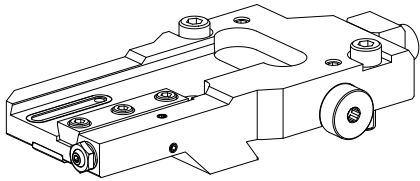
**INDEX**



MS32/MS40

11091545

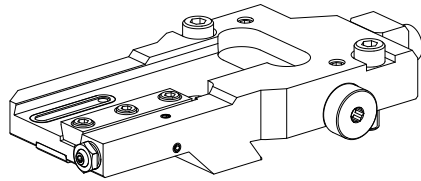
**INDEX**



MS32/MS40

11091545

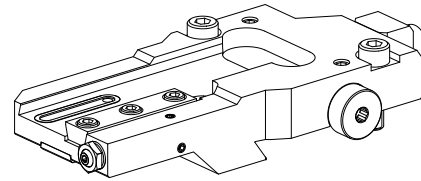
**INDEX**



MS32/MS40

11091545

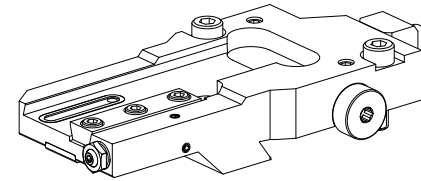
**INDEX**



MS32/MS40

11091545

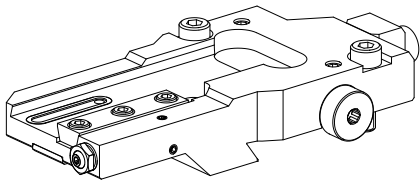
**INDEX**



MS32/MS40

11091545

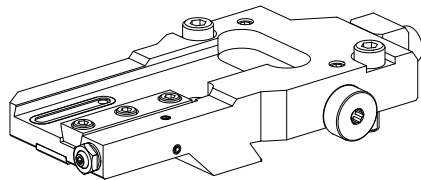
**INDEX**



MS32/MS40

11091545

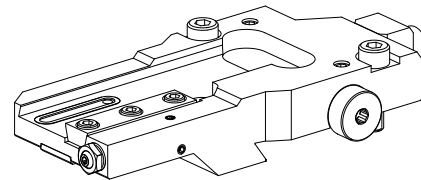
**INDEX**



MS32/MS40

11091545

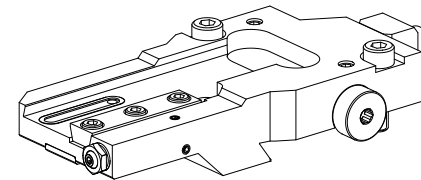
**INDEX**



MS32/MS40

11091545

**INDEX**



MS32/MS40